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Re application of:

John J. Weisgerber, et al.

Group Art Unit: 2858

Examiner:

Serial No.: 09/735,097

Filed: December 11, 2000

For: METHOD AND SYSTEM FOR INSPECTING ELECTRONIC
COMPONENTS MOUNTED ON PRINTED CIRCUIT
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Attorney Docket No.: GSIL 0148 PUS

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
United States Patent and Trademark Office
Washington, D.C. 20231

Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and § 1.97-1.98, the references listed and identified on the attached Form PTO/SB08B are being submitted herewith for consideration by the Examiner.

While this Statement is being filed in compliance with the duty of disclosure, citation of the attached references is not to be construed as an admission that any of the reference(s) are "material" as defined under 37 C.F.R. § 1.56(b).

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8

I hereby certify that this paper, including all enclosures referred to herein, is being deposited with the United States Postal Service as first-class mail, postage pre-paid, in an envelope addressed to: Commissioner for Patents, United States Patent and Trademark Office, Washington, D.C. 20231 on:

August 29, 2001
Date of Deposit

David R. Syrowik
Name of Person Signing

[Signature]
Signature

A copy of each reference listed on the attached Form PTO/SB08B is included herewith. Consideration and entry into the record of these references is respectfully requested.

Respectfully submitted,

John J. Weisgerber, et al.

By: 

David R. Syrowik

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Date: August 29, 2001

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